

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	4893	(318/254).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/04 17:50
S2	2	"20040135528"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/04 17:51
S3	2	"20040135528" and (current\$2 adj source)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/04 17:52
S4	2	"20040135528" and ((current\$2 adj source) adj control)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/04 18:36
S5	4	"6476654"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/04 18:39
S6	4	"6236239"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/04 18:50
S7	72	318/254 and (gate mosfet) and (slew adj rate)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/04 19:15
S8	12	318/254 and (gate mosfet nmos pmos) and (power adj (source line)) and (first adj (power adj (source line))) and (second adj (power adj (source line)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/04 19:34
S9	12	318/254 and (gate mosfet nmos pmos mos) and (power adj (source line)) and (first adj (power adj (source line))) and (second adj (power adj (source line)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/04 19:36
S10	51	"318"/\$ and (gate mosfet nmos pmos mos) and (power adj (source line)) and (first adj (power adj (source line))) and (second adj (power adj (source line)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/05 16:28
S11	39	S10 not S8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/05 13:07
S22	17	"423963"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/05 15:47
S23	51	"5081380"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/05 16:21

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S24	8	"5144157"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/05 16:22
S25	13	"5610546"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/05 16:22
S26	0	("6008686").URPN.	USPAT	OR	ON	2006/08/05 16:27
S27	5	("4980586" "5081380" "5523711" "5586564" "5836691").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/08/05 16:27
S35	85	"327"/\$ and (gate mosfet fet) and current and (threshold maximum limit) and source and (hi high\$2) and (lo low\$4) and (("not"\$2 near4 circuit) (cmos near2 inverter)) and (capacitance) and gate and voltage and ((increas\$4 rais\$4) near4 voltage) and (monolithic)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/05 19:49
S37	127	"327"/\$ and (gate mosfet fet) and current and (threshold maximum limit) and (hi high\$2) and (lo low\$4) and (("not"\$2 near4 (gate circuit)) (cmos near4 invert\$4)) and gate and voltage and ((increas\$4 rais\$4) near4 voltage) and (monolithic)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/05 19:58
S38	42	S37 not S35	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/05 19:50
S39	92	"327"/\$ and (current near4 source) and (threshold maximum limit) and (hi high\$2) and (lo low\$4) and (("not"\$2 near4 (gate circuit)) (cmos near4 invert\$4)) and gate and voltage and ((increas\$4 rais\$4) near4 voltage) and (monolithic)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 07:25
S40	0	S39 not (S37 S35)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/05 20:00
S41	178	"327"/\$ and (current near4 source) and (("not"\$2 near4 (gate circuit)) (cmos near4 invert\$4)) and gate and voltage and (monolithic)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/05 20:00
S42	86	S41 not (S37 S35 S39)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/05 20:00
S43	32	"326"/\$ and (current near4 source) and (threshold maximum limit) and (hi high\$2) and (lo low\$4) and (("not"\$2 near4 (gate circuit)) (cmos near4 invert\$4)) and gate and voltage and ((increas\$4 rais\$4) near4 voltage) and (monolithic)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 07:28
S44	28	"323"/\$ and (current near4 source) and (threshold maximum limit) and (hi high\$2) and (lo low\$4) and (("not"\$2 near4 (gate circuit)) (cmos near4 invert\$4)) and gate and voltage and ((increas\$4 rais\$4) near4 voltage) and (monolithic)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 07:29
S45	65	"331"/\$ and (current near4 source) and (threshold maximum limit) and (hi high\$2) and (lo low\$4) and (("not"\$2 near4 (gate circuit)) (cmos near4 invert\$4)) and gate and voltage and ((increas\$4 rais\$4) near4 voltage) and (monolithic)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 07:31

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S46	144	"331"/\$ and (current near4 source) and (threshold maximum limit) and (hi high\$2) and (lo low\$4) and ("not"\$2 near4 (gate circuit)) (cmos near4 invert\$4) inverter) and gate and voltage and ((increas\$4 rais\$4) near4 voltage) and (monolithic)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 07:31
S47	79	S46 not S45	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 08:59
S48	8	((("6264431") or ("6996333") or ("5760567") or ("6329783")).PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/07 09:46
S49	2	("20020113615").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/07 09:52
S50	17	"423963"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 09:54
S51	35	"4230117"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 09:54
S52	2	"04230117"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 09:55
S53	15	"2244400"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 10:41
S54	2584	(gate adj driver).ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 10:41
S55	681	(((gate adj driver).ab. (gate adj driver).clm.) and inverter	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 10:41
S56	337	(((gate adj driver).ab. (gate adj driver).clm.) and inverter and (mos pmos nmos mosfet)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 10:49
S57	25	(((gate adj driver).ab. (gate adj driver).clm.) and inverter and (mos pmos nmos mosfet) and (ic (integrated adj circuit)) and monolithic	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 10:50
S58	185	(((gate adj driver).ab. (gate adj driver).clm.) and inverter and (mos pmos nmos mosfet) and (ic (integrated adj circuit))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 11:07

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S59	95	((gate adj driver).ab. (gate adj driver).clm.) and inverter and (mos pmos nmos mosfet) and (ic (integrated adj circuit)) and (first) and second and (current near4 source)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 12:52
S60	16	((gate adj driver).ab. (gate adj driver).clm.) and inverter and (mos pmos nmos mosfet) and (ic (integrated adj circuit)) and (first) and second and (current near4 source) and (first adj (current near4 source)) and (second adj (current near4 source))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 13:04
S61	29	((gate near4 driver).ab. (gate near4 driver).clm.) and inverter and (mos pmos nmos mosfet) and (ic (integrated adj circuit)) and (first) and second and (current near4 source) and (first adj (current near4 source)) and (second adj (current near4 source))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 13:23
S62	8	("4532436" "4703199" "4942309" "5325258" "5539334" "5572156" "6043698" "6326831").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/08/07 13:19
S63	9	("6043698").URPN.	USPAT	OR	ON	2006/08/07 13:21
S64	37	((gate near4 driver).ab. (gate near4 driver).clm.) and inverter and (ic (integrated adj circuit)) and (first) and second and (current near4 source) and (first adj (current near4 source)) and (second adj (current near4 source))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 13:24
S65	78	((gate near4 driver)) and inverter and (ic (integrated adj circuit)) and (first) and second and (current near4 source) and (first adj (current near4 source)) and (second adj (current near4 source))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 13:25
S66	69	((gate near4 driver)) and inverter and (ic (integrated adj circuit)) and (first) and second and (current near4 source) and (first adj (current near4 source)) and (second adj (current near4 source)) and (passive or resistor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 13:39
S67	3	"6911858"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 13:29
S68	68	((gate near4 driver)) and inverter and (ic (integrated adj circuit)) and (first) and second and (current near4 source) and (first adj (current near4 source)) and (second adj (current near4 source)) and (resistor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 13:41
S69	18	((gate near4 driver)) and inverter and (ic (integrated adj circuit)) and (first) and second and (current near4 source) and (first adj (current near4 source)) and (second adj (current near4 source)) and (resistor near4 (input))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 13:58
S70	13	"327"/\$ and ((gate near4 driver)) and (ic (integrated adj circuit)) and (current near4 source) and (((first adj (current near4 source)) and (second adj (current near4 source))) or (two adj (current near4 source))) and (resistor near4 (input))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 14:01
S71	55	((gate near4 driver)) and (ic (integrated adj circuit)) and (current near4 source) and (((first adj (current near4 source)) and (second adj (current near4 source))) or (two adj (current near4 source))) and (resistor near4 (input))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 14:05

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S72	38	(US-20010024093-\$ or US-20030034818-\$ or US-20020014900-\$ or US-20020031032-\$).did. or (US-6476654-\$ or US-6437611-\$ or US-6236239-\$ or US-6747425-\$ or US-6407601-\$ or US-6008686-\$ or US-5994937-\$ or US-5610543-\$ or US-5587676-\$ or US-5495184-\$ or US-5453709-\$ or US-5438282-\$ or US-5175452-\$ or US-6154069-\$ or US-5557223-\$ or US-5243227-\$ or US-5214320-\$ or US-5081380-\$ or US-6469585-\$ or US-6724268-\$ or US-6714049-\$ or US-6275121-\$ or US-6947514-\$ or US-5514981-\$ or US-5747943-\$ or US-6359797-\$).did. or (US-6509765-\$ or US-6507226-\$ or US-6043698-\$ or US-5887150-\$ or US-6072742-\$ or US-5495154-\$).did. or (GB-2244400-\$).did. or (GB-2244400-\$).did.	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2006/08/07 14:04
S73	81	((gate near4 driver)) and ((ic (integrated adj circuit)) and (current near4 (supply source)) and (((first adj (current near4 (supply source)))) and (second adj (current near4 (supply source)))) or (two adj (current near4 (supply source)))) and (resistor near9 (input)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 15:30
S74	36	S72 and first and second	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 15:30
S75	36	S72 and first and second and (supply source)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 15:32
S76	30	S72 and first and second and ((supply source) near4 (first second))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 15:33
S77	18	S72 and first and second and (((supply source) near4 (first second)) near4 current)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 15:39
S78	19	S72 and first and second and (((supply source line) near4 (first second)) near4 current)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 15:40
S79	24	S72 and first and second and (((supply source line) near4 (first second)) near4 (power current))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 17:29
S80	23	S72 and first and second and (((supply source line) near4 (first second)) near4 (power current)) and integrated	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 17:30